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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

First-Named Inventor: FLEMING, George
Application No.: 09/517884 Conf.: 5479
Date Filed: 03/03/2000

Atty Docket No.: US00 8002
Art Unit: 2134
Examiner: Zia, Mossadeq

Title: IEEE 1394 LINK LAYER CHIP WITH "5C" AUTHENTICATION AND KEY
EXCHANGE ACCELERATOR

Commissioner for Patents
P.O. Box 1450
Arlington, VA 22313-1450

NOTICE OF APPEAL

Sir:

Applicant(s) hereby appeal(s) to the Board of Patent Appeals and Interferences from the
decision dated 04/05/2004 of the Examiner finally rejecting claims 1-17.

- ☒ Please charge the fee of \$330.00 to Deposit Account No. 14-1270.
☐ No additional fee is required, because the fee was paid in a prior appeal.

Please charge any fees which may be required, except the issue fee, or credit any
overpayment to Deposit Account No. 14-1270.

Date: 6/18/04

Respectfully submitted,

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(Name)	Daniel L. Michalek